	Type	#	Hits	Search Text	DBs	Time Stamp	Comments	Time Stamp   Comments   Error Definition	Err ors
1	BRS	1.1	84	(load same balanc\$3) and time and dispatch and USPAT 09:53 packet and cell	USPAT			0	0
7	BRS	1.2	79		USPAT				0

	Type	#	Type L # Hits	Search Text	DBs	Time Stamp	Comments	Time Stamp Comments Error Definition	Err
1	BRS L1	L1	362	(load same balancing) and packets and ports and (engines or processors) and (queues or memory or buffers or Fifos)	I	2004/07/05 09:32			0
2	BRS	1.2	125	ıme robin)		2004/07/05 09:32			0
3	BRS L3	L3	46	2 and cell and (scheduler cor dispatch\$3)	USPAT	2004/07/05 09:34			0

ors	0	0	0	0	0	0	0	C
Error Delimitation								
			*************					
Comments								
Time Stamp	2004/07/04 08:59	2004/07/04 09:00	2004/07/04 09:02	2004/07/04 09:03	2004/07/04 09:04	2004/07/04 09:04	2004/07/04 09:04	2004/07/04
DBs	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT	דומם בוד
Search Text	(processors or engines) and packets and (load same balanc\$3) and (input or egress) and (output or egress)	<del>/</del>	2 and (memory or buffer or ram or fifo) and bandwidth	3 and stream and flow and controller	4 and count\$3 and assign\$4	5 and byte	6 and link and cell	ן סאט מריין לאפליין
Hits	560	513	394	185	146	131	83	7.7
# T	ĽI	L2	L3	L4	LS	Ре	L7	O L
Туре	BRS	BRS	BRS	BRS	BRS	BRS	BRS	прс
	н	2	3	4,	2	9	7	α